

2020「中线准净线樂盒」

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研究獎學金

Research Scholarship



以金屬矽化物和鍺化物實現低接觸電阻率之下一代元件 Realizing Low Contact Resistivity by Metal Silicide and Germanide for Application on Next Generation Device 國立清華大學 工程與系統科學系 博士班 鄧仕傑

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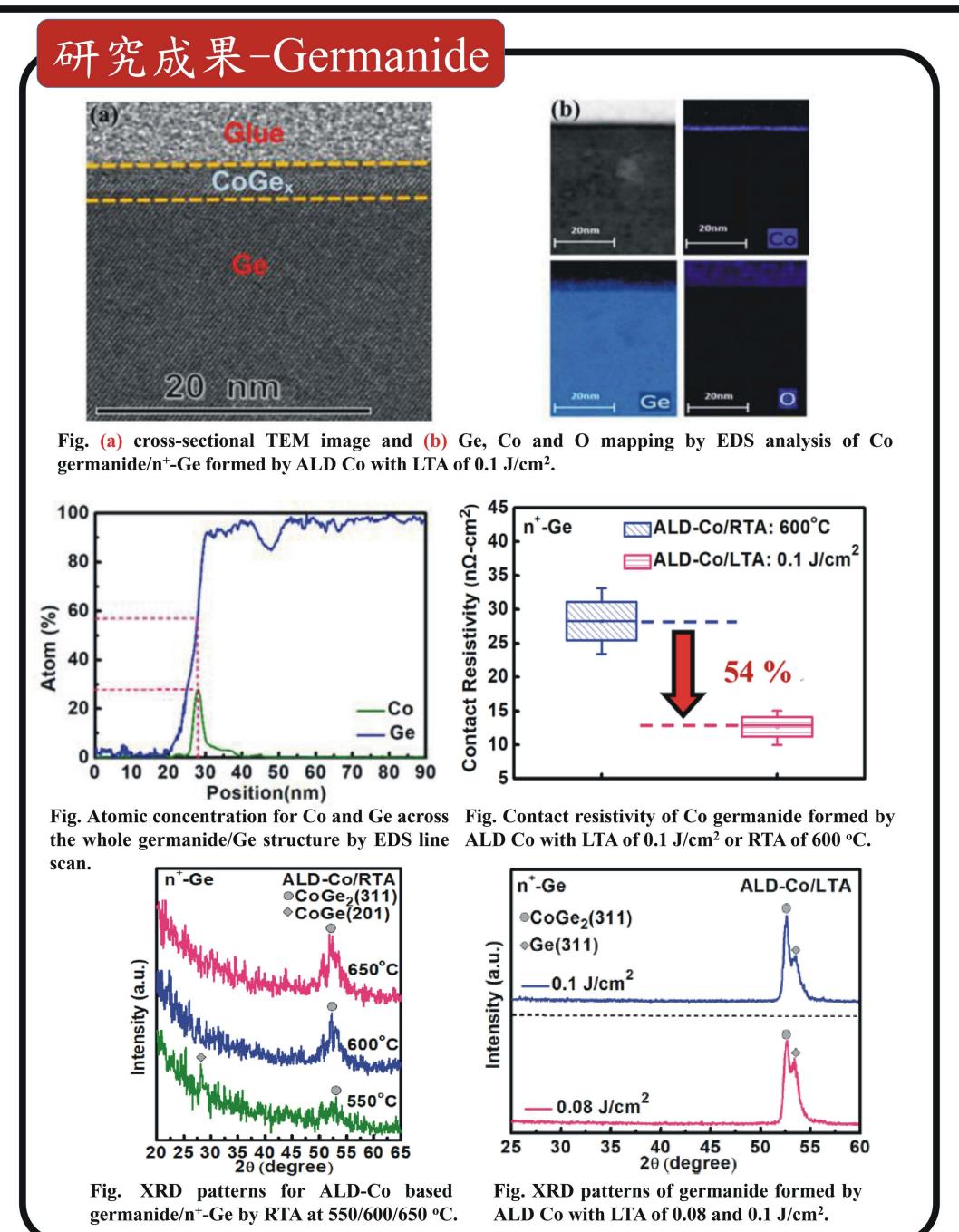


研究重點

My research focuses on the development of metal semiconductor technology with low contact resistivity which is divided into two parts: ALD-Co silicide (part I) and ALD-Co germanide (part II). Part I, ALD-Co silicide is found that most excellent contact resistivity of $1.0 \times 10^{-8} \,\Omega$ -cm² can be achieved due to enhanced crystallinity in CoSi₂ phase by RTA at 750 °C. Based same RTA temperature to compare PVD-Co silicide, ALD-Co silicide exhibits reduced contact resistivity by 76 % and it is mainly due to the optimized interface roughness between silicide and Si substrate that makes less roughness induced scattering effect. Part II, ALD-Co germanide was formed by RTA and LTA, respectively which explored its impact on the characteristics of CoGe_x on n⁺-Ge. Compared to RTA, LTA exhibits a nearly epitaxial film and a low contact resistivity of $1.3 \times 10^{-8} \,\Omega$ -cm². The promising results can be attributed to the low thermal budget with significant thermal gradient/shallow heat distribution of LTA, proving the eligibility of the contact technology beyond 5 nm node.

研究成果-Silicide ALD Co/RTA-75 Fig. TEM image of ALD-Co based silicide formed by RTA at (a) 750 °C and (b) 800 °C. ALD-Co/RTA n*-Si PVD-Co/RTA ©CoSi₂(111) ALD-Co/RTA RTA: 750 °C Resistivity 30 76 % 2θ (degree) Fig. Contact resistivity of PVD-Co and ALD-Co Fig. XRD patterns of ALD-Co based silicide formed by RTA at various RTA temperatures. based silicide formed by RTA at 750 °C. PVD Co RMS: 2.645 nm ALD Co RMS: 1.004 nm

Fig. Comparison of AFM topology of Si surface after stripping Co silicide formed by PVD and



研究生活與心得

ALD Co with RTA at 750 °C.

在求學過程中,感謝中技社肯定我在研究所期間的學術成果。學生銘記且感謝指導教授巫勇賢老師悉心的指導與研究上給予的資源,使我在博士班的求學道路上能有實現自我理想的舞台,並展現豐碩的研究成果。除此之外,也感謝研究所一路相伴的實驗室夥伴給予正面能量與歡笑,共創良好的研究環境一同前進與努力。

