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Research Scholarship for International Graduate Students



Energy Band Engineering of III-Nitride High Electron Mobility Transistors for Power and RF Applications

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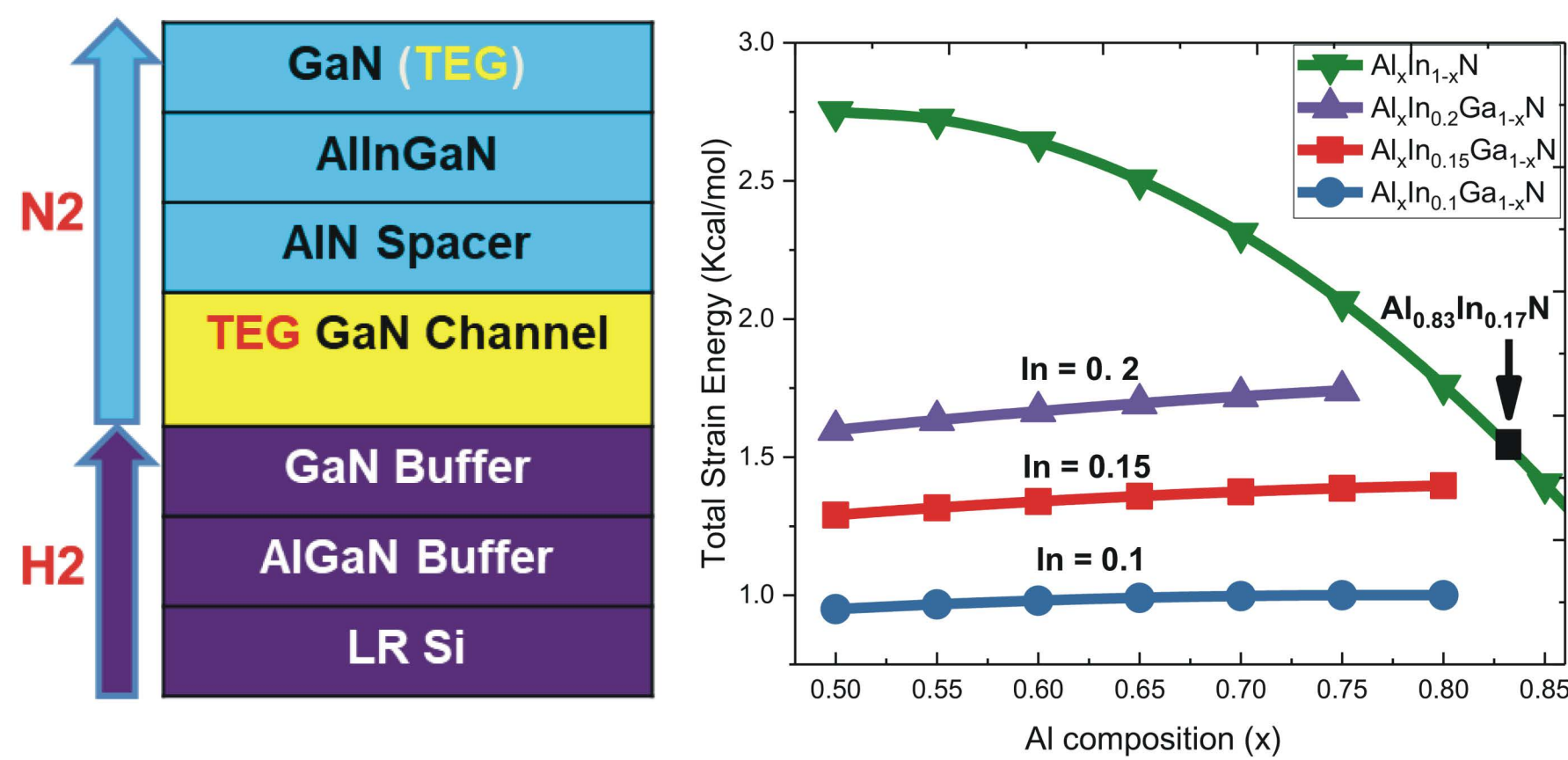
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Aim and Scope

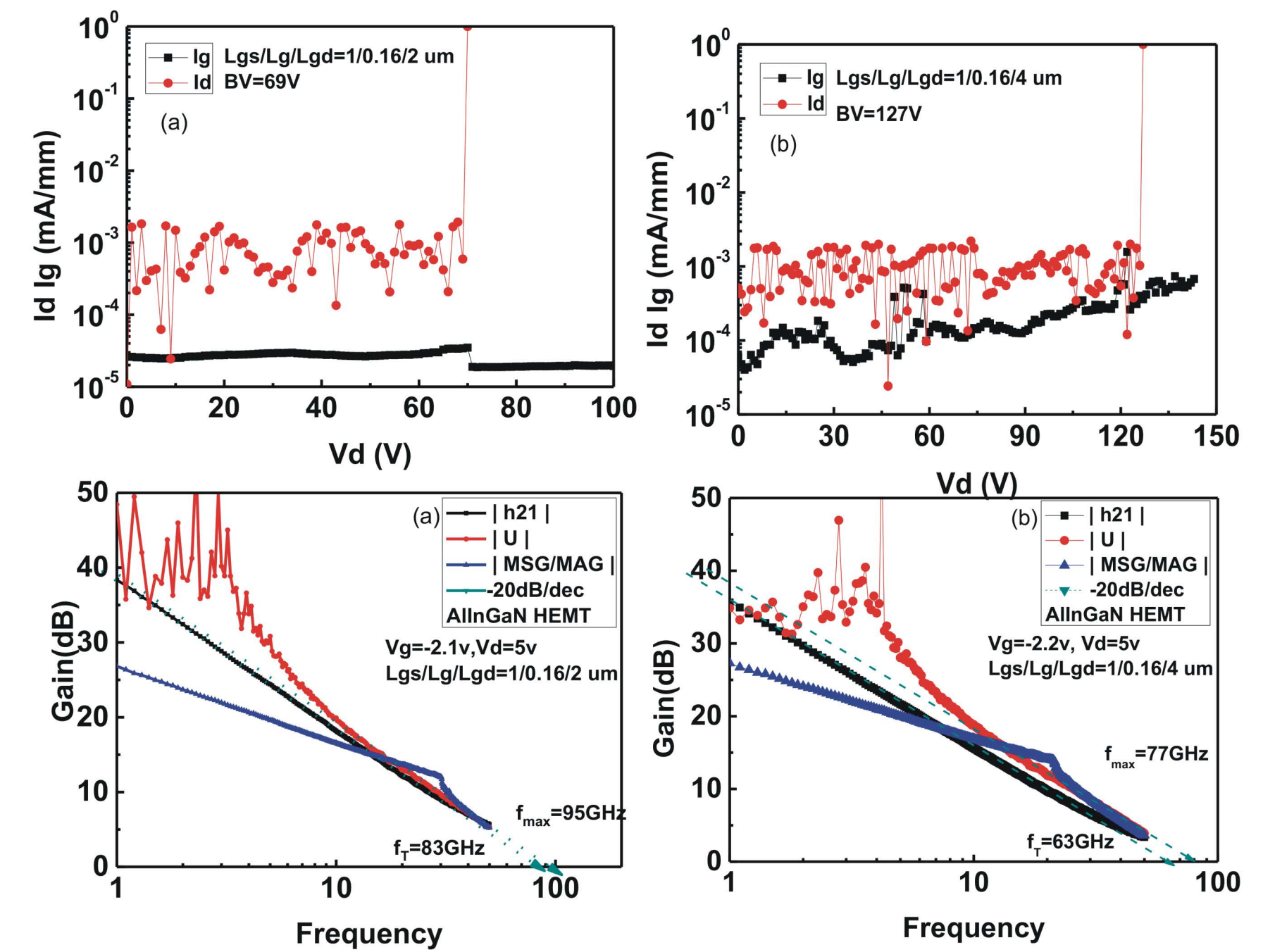
Our research is helpful in developing cost effective high power mm-wave GaN high electron mobility transistors (HEMTs) on low cost large diameter 150 mm low resistivity silicon substrates for 5G applications. The aim is to grow very high quality epitaxial layers and find a way to eliminate parasitic conduction at the substrate/buffer junction to minimize large signal power loss at high operating frequency. Furthermore, the research on emerging B(AI,Ga)N/(B)GaN heterostructures may open new opportunities in designing the next generation high current density and/or high power RF transistors due to the high two dimensional electron gas (2DEG) at the B(AI,Ga)N/(B)GaN heterointerface and lattice matched barrier/buffer with the SiC substrates.

AllnGaN Heterostructure and its Thermodynamic Stability



- ✓ TEG grown GaN channel in N₂ ambient significantly reduces C-incorporation up to an order of magnitude as compared to TMG grown GaN in H₂ ambient
- ✓ A slow change in stability when the Ga atoms replace Al atoms
- ✓ Significant improvement in stability when indium atoms are replaced by Ga atoms owing to significant reduction in total elastic strain in the barrier

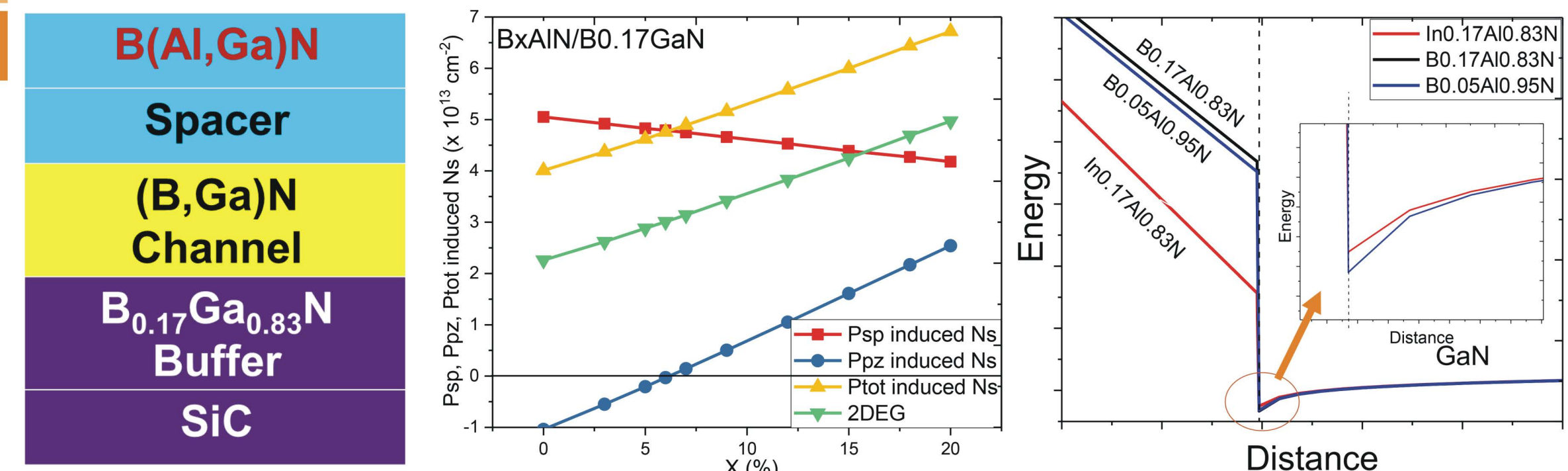
DC and RF Device Performance



Sample Lgs/Lg/Lgd (μm)	BV (v)	f _t /f _{max} (GHz)	JFOM (THz-V)
1/0.16/2	69	83/95	5.7
1/0.16/4	127	63/77	8.1

- ✓ The current gain cut-off frequency of 83 GHz and 63 GHz and the power gain cut-off frequency of 95 GHz and 77 GHz respectively are obtained, result in Johnson's figure of merit of 5.7 THz-V and as high as 8.1 THz-V respectively.
- ✓ The f_T and J-FOM are comparable with the reported values obtained on high resistive silicon for devices with similar gate length.

2DEG formation and Band structure of B(AI,Ga)N/(B)GaN



- ✓ Spontaneous polarization (Psp) induced carrier concentration (Ns) decreases from 5.05x10¹³ cm⁻² to 4.18 x10¹³ cm⁻² with increasing B incorporation from 0 to 20% in barrier
- ✓ Piezoelectric polarization (Ppz) induced carrier concentration increases from -1.04 x10¹³ cm⁻² to 2.54 x10¹³ cm⁻² with same increase in B composition
- ✓ 2DEG formed at heterointerface increases almost linearly from 2.26 x10¹³ cm⁻² to 4.97 x10¹³ cm⁻² as B incorporation varies from 0 to 20% in barrier
- ✓ Ppz induced Ns becomes zero at around 6% boron incorporation in barrier, indicating no strain due to lattice matched barrier and buffer
- ✓ B_{0.05}Al_{0.95}N exhibits much higher Schottky barrier height as compared Al_{0.83}In_{0.17}N barrier
- ✓ Schottky barrier height increases with increasing B incorporation in the barrier

Summary

AllnGaN/GaN heterostructures on 150 mm low resistivity silicon substrates have been grown with state of the art transport properties for mm wave transistor applications. A new model is proposed, correlating thermodynamic stability and electron scattering mechanisms. Electron mobility around 2100 cm²/V-s with 2DEG density over 1x10¹³ cm⁻² is achieved. Fabricated devices exhibit Johnson's figure of merit over 8 THz-V, comparable or better than GaN HEMT on high resistivity Si substrate. Further, proposed a B(AI,Ga)N heterostructure with both lattice matched barrier and buffer to SiC for high power/high frequency transistors. It is analytically shown that these novel heterostructures not only exhibit higher schottky barrier height to the injecting electron from the gate but also exhibit much higher 2DEG than the lattice matched AllnN/GaN HEMT and therefore, could be a promising alternative for next generation power/RF HEMTs



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